R18 Code No: 156CW JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech III Year II Semester Examinations, February - 2023 SOFTWARE TESTING METHODOLOGIES (Common to CSE, IT)

Time: 3 Hours

Max. Marks: 75

Note: i) Question paper consists of Part A, Part B.

- ii) Part A is compulsory, which carries 25 marks. In Part A, Answer all questions.
- iii) In Part B, Answer any one question from each unit. Each question carries 10 marks and may have a, b as sub questions.

PART – A

		(25 Marks)
1.a) b) c) d) e) f) g) h) i) j)	Define path testing. What is the difference between an error and a bug? Write a short note on random testing. What is the significance of data flow testing? Write a short note on path expressions. List out the different operators that are used to solve any boolean algebra. Define a transition bug. What is good state graph? Define a connection matrix. List the applications of grant matrices.	[2] [3] [2] [3] [2] [3] [2] [3] [2] [3]
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2 ~)	Differentiate William data and debugging	(50 Marks)
2.a) b)	Differentiate between testing and debugging. Describe the model for testing.	[5+5]
,	OR	
3.	Classify the different types of bugs and explain.	[10]
4.a) b)	State and explain the transaction flow testing techniques. Compare static slicing with dynamic slicing. OR	[6+4]
5.	How developers and testers treat nice and ugly domains? Illustrate we examples.	ith the help of [10]
6.a) b)	Illustrate maximum path count arithmetic with an example. Describe the usage of regular expression in flow anomaly detection. OR	[6+4]
7.a) b)	Justify the use of decision table implementation for designing test cases. Explain the procedure for specification validation using KV charts.	[5+5]

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8.	Explain the following terms:	
	a) Design guideline for building finite state machine	
	b) Inessential finite state behavior.	[5+5]
	OR	
9.a)	Write short notes on testability tips.	
b)	Summarize the concept transition testing.	[5+5]
10.a)	Describe node-term reduction optimization.	
b)	Give a brief summary on relations.	[5+5]
	OR	
11.	Write an algorithm for node reduction using matrix operations and explain.	[10]